




K.V.G. COLLEGE OF ENGINEERING

Kurunjibhag, Sullia, D.K.-574 327, Karnataka, INDIA
 E-mail: office@kvgengg.com website: www.kvgengg.com

Department of Electronics and Communication Engineering

FACULTY PROFILE

| | | |
|---------------------|--------------------------|---|
| Name of The Faculty | ADARSHA. D |  |
| Designation | Assistant Professor | |
| Contact Numbers | MOBILE: +91 - 9731157034 | |
| E-Mail ID | adarsha322@yahoo.in | |

• Educational Qualification

| Degree | Specialization | Year Of Passing | College & University |
|---------|---|-----------------|--|
| D.E | Electronics and Communication Engineering | 2004-2007 | Vivekananda Polytechnic Puttur & Board of Technical Education, Bangalore |
| B.E | Electronics And Communication Engineering | 2007-2010 | PA College of Engineering Mangalore & VTU, Belagavi. |
| M. Tech | VLSI Design and Embedded System | 2011-2013 | KVG College of Engineering Sullia & VTU, Belagavi. |

• Work Experience

| Teaching | Research | Industry |
|---------------|----------|--------------|
| 7 +Experience | ----- | 1+Experience |

• Date of Joining to the College

06th August 2014

• In-House Experience

| Designation | Duration | | Department |
|-----------------|----------------------------|----|------------|
| | From | To | |
| Asst. Professor | 06-08-2014 - Till The Date | | E & C |

• Outside Experience

| Designation | Place | Duration | |
|-----------------|---------------------------|-----------|----------|
| | | From | To |
| Asst. Professor | K.I.T, Mangalore | 1-8-2013 | 1-8-2014 |
| Design Engineer | APLOMB Solution Pvt., Ltd | 30-7-2010 | 1-8-2011 |

• Area of Interest

| | | | | | |
|-------------------|------------------------|-----------------------------------|---------------------------------|-----|-----------------------|
| Power Electronics | Wireless Communication | Broad Band Wireless Communication | Advanced Communication Networks | GSM | IOT & Cloud Computing |
|-------------------|------------------------|-----------------------------------|---------------------------------|-----|-----------------------|

• Subject Taught

| | |
|--|---|
| <p>For UG:</p> <ul style="list-style-type: none"> • Power Electronics • Basic Electronics • Verilog HDL • Microprocessor • Digital Communication • Wireless Communication • GSM • Embedded System Design • Microcontroller | <p>For PG:</p> <ul style="list-style-type: none"> • ASIC design • SOC Design • Low Power VLSI Design • Wireless Broadband LTE 4G • LTE 4G Broadband • Digital commutation system 2 |
|--|---|

| • Publications | | | |
|---|--|---|--|
| National / International Journals | | National / International Conferences | |
| 1 | | 1 | |
| • Publications : International Journals | | | |
| 1. Adarsha. D And Pradeep T.M, "Image Water Marking Using Spread Spectrum Technology" <i>International Journal of (IIES)</i> , Vol. 06, No.8, pp. 30-05-2013 | | | |
| • Publications : National / International Conferences | | | |
| 1. Path Finding Robot, "Jnanasangama -17", 2017. | | | |
| • Professional Activities: NPTEL Course | | | |
| NPTEL (National Program On Technology Enhanced Learning) | Subject | Year Of Certification | Result |
| | Wireless And Communication | 2018 | 60% |
| • Faculty Development Programmes (FDP) Participated | | | |
| Sl. No. | Program Title | Venue | Date & Year |
| 1. | Student Induction Program | Sahyadri College Of Engineering ,MITE, Mangalore | 2019 |
| 2. | Train The Trainer | Tessolve Semiconductor Pvt, .Ltd Bangalore | 28-05-2018 to 02-06-2018 |
| 3. | Advanced Facilitation Skills | KVGCE, Sullia | 5 th &6 th Feb 2018 |
| 4. | Experimental Learning Through Virtual Lab | KVGCE, Sullia | 18 th April 2018 |
| 5. | Li2-Android (Building Mobile Phone Applications) | KVGCE, Sullia | 2 nd &3 rd April 2012 |
| 6. | Understanding Self And Interpersonal Relationship | KVGCE, Sullia | 18.12.14 to 19.12.14 |
| 7. | Advances On Embedded System-Real Time Embedded Technology And Applications | Canara Engineering College Mangalore | 25.01.16 to 27.01.2016 |

| | | | |
|----|---|---------------|------------|
| 8. | Experiential Learning Through Virtual Lab | KVGCE, Sullia | 18.04.2018 |
| 9. | Real Time Sensor Technologies | KVGCE, Sullia | 28.09.2018 |

• Workshops / Short-Term Course Attended

| Sl. No. | Workshop Title | Place | Date & Year |
|---------|-------------------|--|-----------------------------|
| 1. | Train The Trainer | Tessolve Semiconductor Pvt .Ltd Bangalore | 28.05.2018 to 02.06.2018 |

• No. Of Project Guided

| UG | PG |
|----|----|
| 8 | 1 |

• Responsibilities Taken in College Level

| Event | Positions | Duration | |
|-------------------|----------------------|----------|-----------|
| | | From | From |
| NBA | Criteria -7 | 2018 | Till Date |
| Student Mentoring | Student Observations | 2014 | Till Date |

• Responsibilities Taken In Department Lev

| Event | Role | Duration | |
|-------------|--------------|----------|------|
| | | From | To |
| 1. VLSI Lab | In- Charge | 2016 | 2017 |
| 2. ENCEA | Co-ordinator | 2017 | 2018 |

• Participation Details In The College Level Event

| Sl.No. | Event | Position / Role |
|--------|--------------------------------|-----------------|
| 1. | College Sports Meet | Member |
| 2. | Aluminee Meet | Member |
| 3. | College Anti-Ragging Committee | Member |
| 4. | College Day Function | Member |
| 5. | Project Exhibition "Expo" | Member |

| | | |
|----|-----------------------------|-------------------------|
| 6. | College Cultural Functions | Member |
| 7. | College Day Function | Member – Food Committee |
| 8. | Science Talent Hunt Program | Member |

• **Participation Details In The Department Level Activities**

| Sl.No. | Event | Role / Responsibilities |
|--------|------------------|---|
| 1. | ENCEA 2017-18 | Co-Ordinator |
| 2. | ENCEA Activities | Technical And Cultural Activities |
| 3. | Counselling | Counselor for EC Students |
| 4. | Mentor | Mini-Project Work 3 rd Semester Students |

• **No. Of Lab Manual / Program Guidelines Prepared**

| Sl. No. | Lab Manual / Guide Lines Title | Semester And Branch | Year |
|---------|--------------------------------|-------------------------|------|
| 1. | VLSI Lab | 7 th Sem E&C | 2017 |

• **No. of Labs Handled For UG and PG**

| Sl. No. | Lab Title | Year | Semester & Branch |
|---------|-------------------------|------------------|-------------------------------------|
| 1. | Power Electronics Lab | 2014-15, 2015-16 | 7 th A & B |
| 2. | Microprocessor Lab | 2014-15, 2015-16 | 6 th B |
| 3. | Power Electronics Lab | 2016-17 | 7 th |
| 4. | Microcontroller Lab | 2016-17 | 4 th B & 6 th |
| 5. | Microprocessor Lab | 2016-17 | 4 th |
| 6. | Power Electronics Lab | 2017-18 | 7 th B |
| 7. | Embedded Controller Lab | 2017-18 | 6 th |
| 8. | Power Electronics Lab | 2018-19 | 7 th |
| 9. | Embedded Controller Lab | 2018-19 | 6 th |

• **Awards / Recognition / Achievements/ Others**

- Served As VTU **Digital Valuation Centre**, KVGCE, and Sullia for The Digital Valuation Work of UG/PG Answer Scripts May/June/July 2016-2017 Examination.

- Served as **Squad Member** of VTU Examination Work.

Mr.ADARSHA D., ASSISTANT PROFESSOR.,E&C DEPT., KVGCE, SULLIA, D.K - 574 327

Reach me at adarsha322@yahoo.in